

In Specification:

Please replace the paragraph from line 14 to line 22 on page 1 with the following:

Figure 1 illustrates a prior art LVDS driver 20 22 and receiver 22 24 connected via differential lines 25. A 100 Ohm differential impedance 26 is placed between the lines 25. The driver 20 22 includes a current source 27 that drives one of the differential lines 25. The receiver 24 has a high DC impedance (it does not source or sink DC current), so the majority of driver current flows across the 100 Ohm termination resistor 26 generating, in this embodiment, approximately 350mV across the inputs of the receiver 24. When the opposite transistors of the driver 22 (the “-” transistors instead of the “+” transistors) are activated, current flows in the opposite direction. In this way, valid digital high and low states are transported.